AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. **(Currently Amended)** A thin film transistor panel, comprising:

a gate line with a gate electrode on a substrate;

a gate insulating layer on the gate line;

a semiconductor layer on the gate insulating layer;

a conductive pattern layer with source and drain electrodes spaced apart on the semiconductor layer, the conductive pattern layer comprising a first layer of molybdenum, an Ag-Al alloy layer which contains about 5 to about 10 at% of silver on the first layer and a second layer of molybdenum on the Ag-Al alloy layer, wherein the first layer of molybdenum has a thickness of about 150 to 700 Angstroms, the Ag-Al alloy layer has a thickness of about 1000 to 3000 Angstroms and the second layer of molybdenum has a thickness of about 300 to 1000 Angstroms;

a passivation layer on the semiconductor layer and the conductive pattern layer, the passivation layer having a plurality of contact holes; and

a plurality of pixel electrodes on the passivation layer, each of the pixel electrodes extending into one of the contact holes to contact the second layer.

2. (original) The thin film transistor panel as claimed in claim 1, wherein the gate line comprises an Ag-Al alloy layer on the substrate and a molybdenum layer on the Ag-Al alloy layer.

3-4. (canceled)

5. (original) The thin film transistor panel as claimed in claim 1, wherein the Ag-Al alloy layer contains about 10 at% of silver.

6. **(Currently Amended)** A liquid crystal display, comprising:

a top plate comprising a transparent electrode;

a bottom plate comprising reflective electrodes of an Ag-Al alloy containing about 10 at% of silver, the reflective electrodes having a visible light reflectance greater than 95% while being observed at wavelength 550nm; and

a liquid crystal layer sandwiched between the top plate and the bottom plate,

wherein an image is generated by the liquid crystal display when ambient light is incident to the surface of the top plate.

7. (previously presented) The liquid crystal display as claimed in claim 6, further comprising a light source below the bottom plate wherein each of the reflective electrodes has at least one aperture defined therein such that an image is generated by the liquid crystal display when light from the light source passes through the apertures of the reflective electrodes.

8. (previously presented) The liquid crystal display as claimed in claim 7, wherein the bottom plate further comprises:

a plurality of parallel gate lines;

a plurality of parallel data lines formed perpendicular to the gate lines, the gate lines and the data lines being arranged to form a matrix of pixel regions with each of the pixel regions bounded by two adjacent said gate lines and two adjacent said data lines; and

a plurality of thin film transistors formed at intersections of said gate lines and data lines,

wherein each of the reflective electrodes is respectively disposed in one of the pixel regions and functions as a pixel electrode.

9-11. (canceled)

12. **(Currently Amended)** The liquid crystal display as claimed in claim 6, wherein the ~~annealed~~ Ag-Al alloy is annealed at temperatures from about 200°C to about 250°C such that the reflective electrodes have a visible light reflectance greater than 97%.

13-16. (canceled)

17. **(Currently Amended)** A thin film transistor panel, comprising:

a gate line with a gate electrode on a substrate, the gate line comprising an Ag-Al alloy layer which contains about 5 to about 10 at% of silver on the substrate and a layer of molybdenum on the Ag-Al alloy layer, wherein the layer of molybdenum has a thickness of about 150 to 700 Angstroms and the Ag-Al alloy layer has a thickness of about 1000 to 5000 Angstroms;

a gate insulating layer on the gate line;

a semiconductor layer on the gate insulating layer;

a conductive pattern layer with spaced apart source and drain electrodes on the semiconductor layer;

a passivation layer on the semiconductor layer and the conductive pattern layer, the passivation layer having a plurality of contact holes; and

a plurality of pixel electrodes on the passivation layer, each of the pixel electrodes extending into one of the contact holes and directly contacting the conductive pattern layer ~~of molybdenum~~.

18. (previously presented) The thin film transistor panel as claimed in claim 17, wherein the conductive pattern layer comprises a first molybdenum layer, an Ag-Al alloy layer on the first molybdenum layer and a second molybdenum layer on the Ag-Al alloy layer.

19-21. (canceled)

22. **(Currently Amended)** A liquid crystal display, comprising:

a top plate comprising a transparent electrode;

a bottom plate comprising reflective electrodes of an Ag-Al alloy containing about 5 at% of silver, the reflective electrodes having a visible light reflectance greater than 95% while being observed at wavelength 550nm; and

a liquid crystal layer sandwiched between the top plate and the bottom plate,

wherein an image is generated by the liquid crystal display when ambient light is incident to the surface of the top plate.

23. **(Currently Amended)** The liquid crystal display as claimed in claim 22, wherein the Ag-Al alloy is annealed at temperatures from about 200°C to about 250°C such that the reflective electrodes have a visible light reflectance greater than 97%% while being observed at wavelength 550nm.